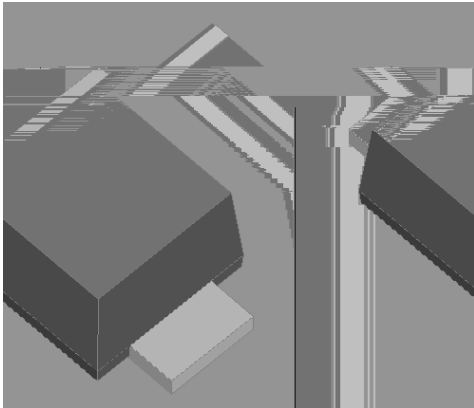
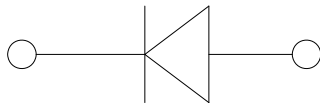




- Low profile package
- Ideal for automated placement
- Guardring for overvoltage protection
- Low power losses, high efficiency
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

For use in low voltage high frequency inverters,
freewheeling diode / Cathode

• Polarity: $\dot{Y}$



Repetitive peak reverse voltage	VRRM	V	40
Average rectified output current @60Hz sine wave, Resistance load	IO	A	2.0
Surge(non-repetitive)forward current @ 60Hz half-sine wave,1 cycle, Tj=25	IFSM	A	35
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25			70
Current squared time @1ms≤t≤8.3ms Tj =25 Rating of per diode	I2t	A2S	5.08
Typical junction capacitance @Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	Cj	pF	67 T Ra e Tj % -55 ~+150

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T_j=25 Unless otherwise specified

Peak Forward Voltage	V _F	V	I _{FM} =2.0A T _j =25	-	0.56	0.60
Maximum DC reverse current at rated DC blocking voltage per diode @ V _{RM} =V _{RRM}	I _{RRM1}	mA	V _{RM} =V _{RRM} T _j =25	-	-	0.05
	I _{RRM2}		V _{RM} =V _{RRM} T _j =100	-	-	10

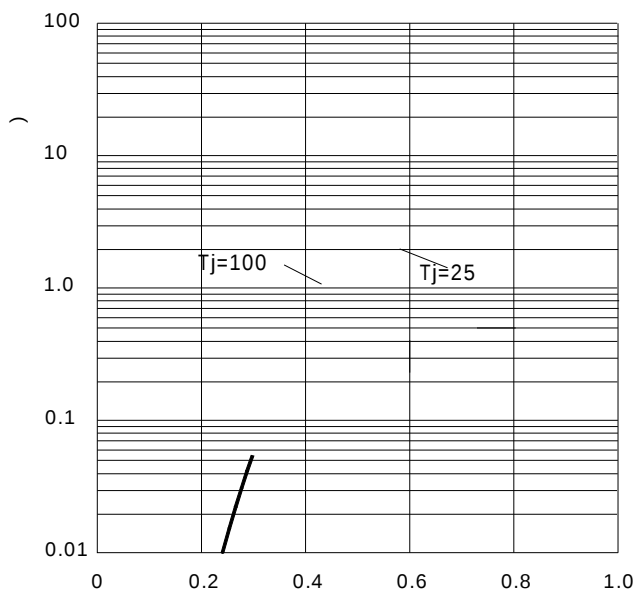
Note1:Pulse test:300uS pulse width,1% duty cycle

Note2:Pulse test:pulse width 40mS

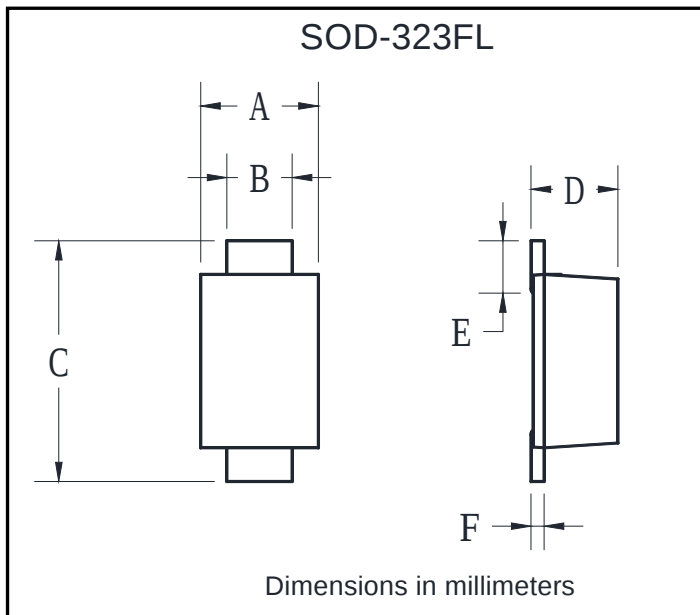
■

T_j=25 Unless otherwise specified

	R _{θJ-A}		90
Thermal Resistance		/W	



■



Dim	Min	Max
A	1.05	1.45
B	0.90	1.15
C	2.30	2.70
D	0.80	1.20
E	0.25	0.70
F	0.05	0.25



Dim	Millimeters
P1	1.30
Q1	1.00
Q2	1.50



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