

Typical Applications

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer and telecommunication.

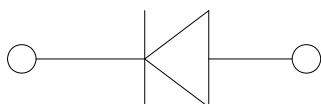
Mechanical Data

Package: DO-214AB (SMC)

Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free

Terminals: Tin plated leads, solderable per J-STD-002 and JESD22-B102

Polarity: Color band denotes the cathode end



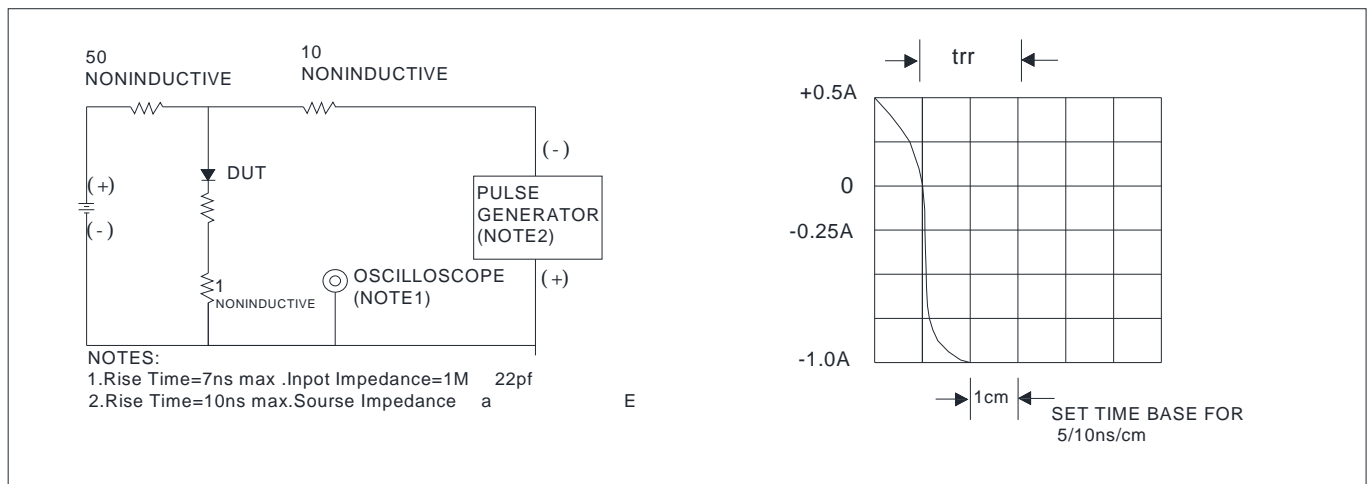
Maximum Ratings ($T_a=25$ Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	MURS360
Device marking code			MURS360
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	V	600
Maximum RMS Voltage	V_{RMS}	V	420
Maximum DC blocking Voltage	V_{DC}	V	600
Average Rectified Output Current @60Hz sine wave, Resistance load, TL (FIG.1)	I_O	A	3.0
Forward Surge Current (Non-repetitive) @60Hz Half-sine wave, 1 cycle, $T_j=25$	I_{FSM}	A	100
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, $T_j=25$			200
Current squared time @1ms t 8.3ms $T_j=25$	I^2t	A^2s	41.5
Storage Temperature	T_{stg}		-55 ~ +150
Junction Temperature	T_j		-55 ~ +150

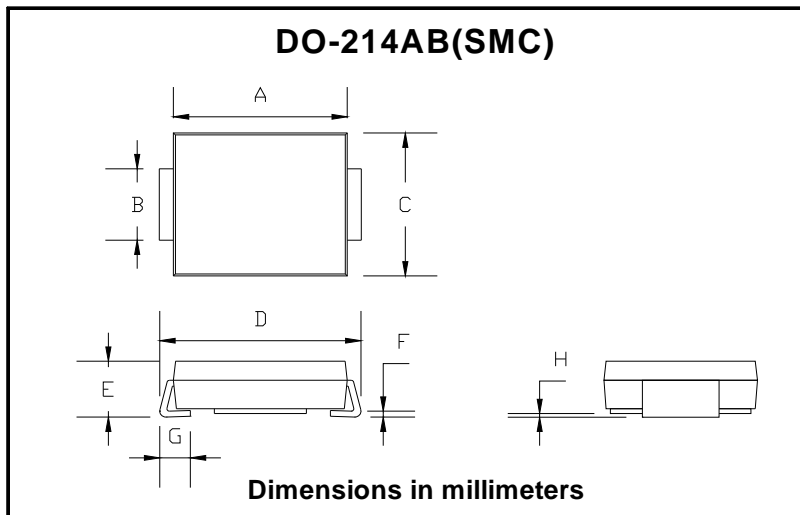
Electrical Characteristics $T_a=25$ Unless otherwise specified

PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	MURS360
Maximum instantaneous forward voltage	V_F	V	$I_{FM}=3.0A$	1.25
Maximum reverse recovery time	t_{rr}	ns	$I_F=0.5A, I_R=1.0A,$ $I_{rr}=0.25A$	50
Maximum DC reverse current at rated DC blocking voltage	I_R	μA	$T_j = 25$	5
			$T_j = 125$	50
Typical junction capacitance	C_j	pF	Measured at 1MHz and Applied Reverse Voltage of 4.0 V.D.C	50

FIG.5: Diagram of circuit and Testing wave form of reverse recovery time



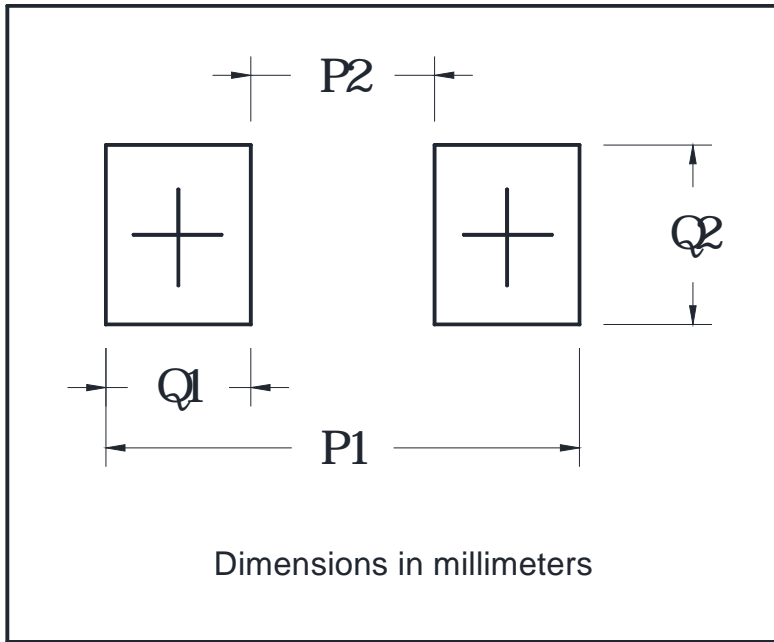
Outline Dimensions



DO-214AB (SMC)		
Dim	Min	Max
A	6.60	7.11
B	2.85	3.27
C	5.59	6.22
D	7.75	8.13
E	1.99	2.61
F	0.15	0.31
G	0.76	1.52
H	0.05	0.20



Suggested pad layout



DO-214AB (SMC)	
Dim	Min
P1	9.9
P2	3.84
Q1	3.03
Q2	3.82



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