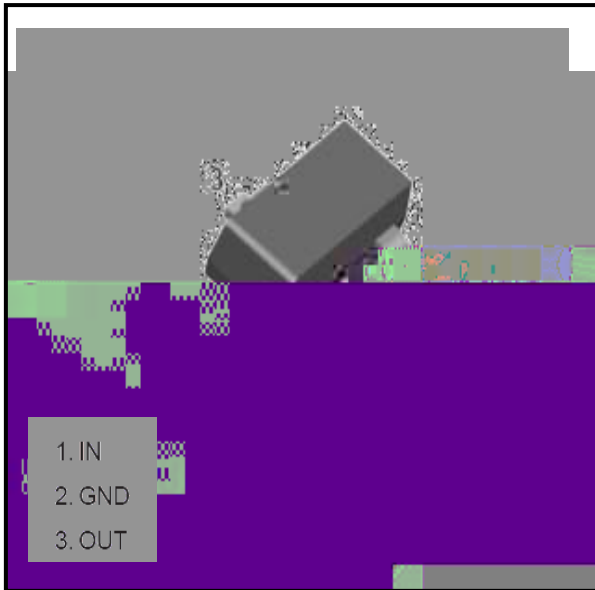


## Digital Transistors (Built-in Resistors)

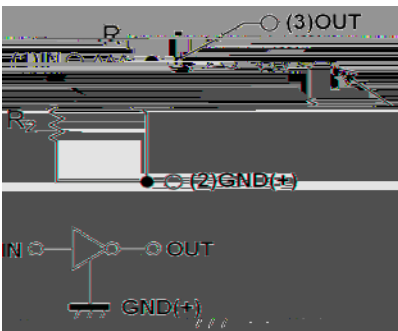


### Features

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion
- PNP
- J-STD-002 and JESD22-B102

E13

### Equivalent circuit



### Maximum Ratings (Ta=25 Unless otherwise specified)

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	$V_{CC}$	V		-50
Input Voltage	$V_{IN}$	V		-30 to +5
Output Current	$I_o$	mA		-100
Power Dissipation	$P_D$	mW		200
Junction Temperature	$T_J$			150
Storage Temperature	$T_{STG}$			-55 to +150



# DTA143ZCA

## Electrical Characteristics (Ta=25 unless otherwise specified)

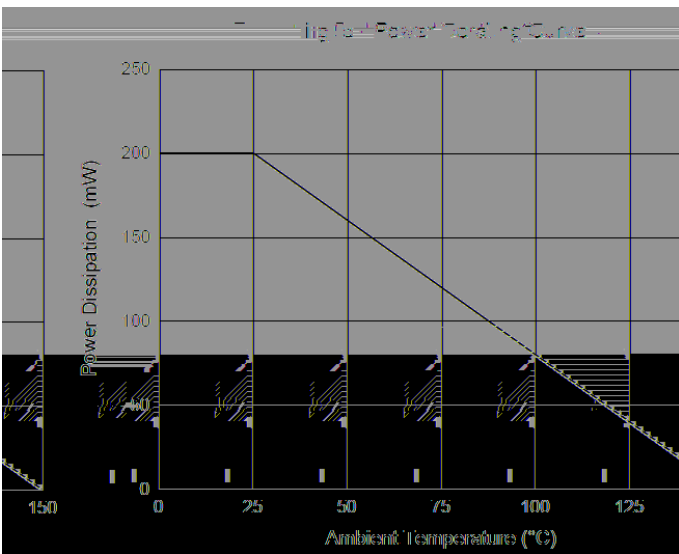
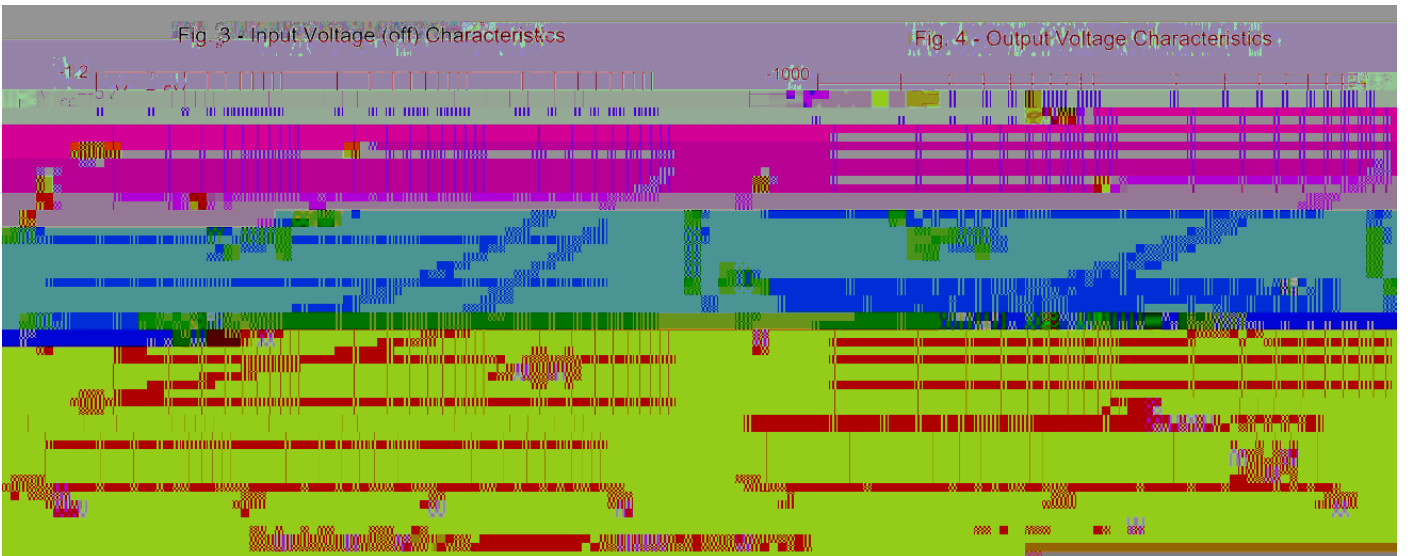
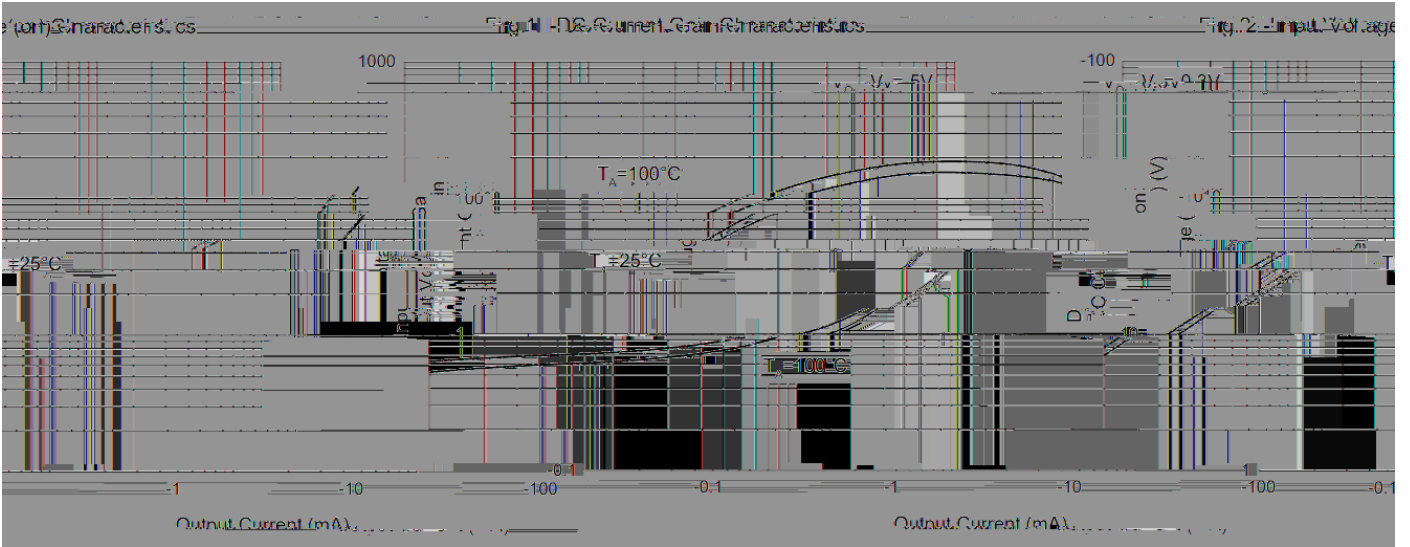
ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	
Input voltage	$V_{I(off)}$	V	$V_{CC}=-5V, I_c=-100\mu A$	-0.5		
	$V_{I(on)}$	V	$V_o=-0.3V, I_c=-2mA$			-1.3
Output voltage	$V_{O(on)}$	V	$I_o / I_i = -10mA / -0.5 mA$			-0.3
Input current	$I_I$	mA	$V_i=-5V$			-1.8
Output current	$I_{O(off)}$	$\mu A$	$V_{CC}=-50V, V_i=0$			-0.5
DC current gain	$G_I$		$V_o=-5V, I_o = -10mA$	80		
Input resistance	$R_1$	k		3.29	4.7	6.11
Resistance ratio	$R_2/R_1$			8	10	12
Transition frequency	$f_T$	MHz	$V_{CE}=-10V, I_E=5mA, f=100MHz$		250	

## Ordering Information (Example)

PREFERRED P/N



Characteristics (Typical)







## Disclaimer

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