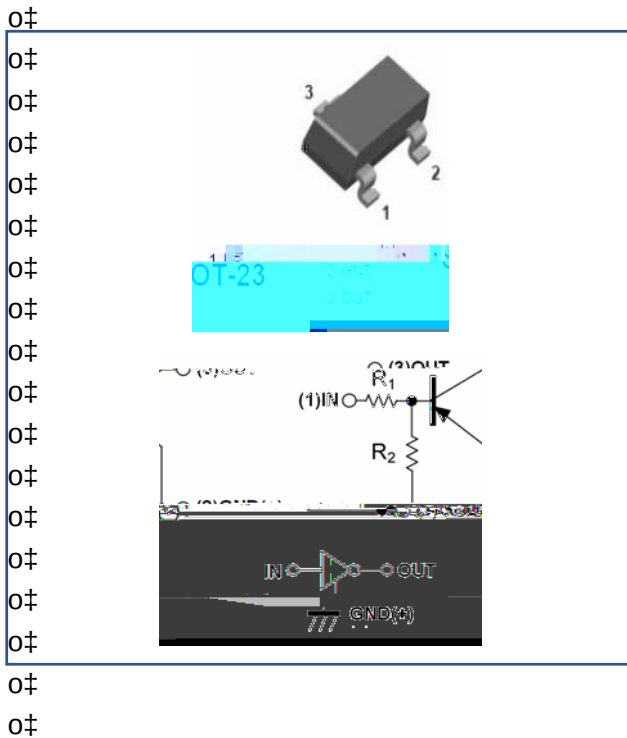


PNP Digital Transistors (Built-in Resistors)



Features

$$\begin{matrix} Z \\ Z \\ Z \end{matrix}$$

Application

$$\begin{matrix} Z \\ Z \end{matrix}$$

Mechanical data

- z Package
- z Terminals

Maximum Ratings

[illegible]



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COMPLIANT

0‡

Electrical Characteristics

Item	Symbol	Unit	Conditions	Min	Typ	Max

Thermal Characteristics

Parameter	Symbol	Unit	Value

Ordering Information

Preferred P/N	Packing code	Unit weight(g)	Minimum package(pcs)	Inner box quantity(pcs)	Outer carton quantity(pcs)	Delivery mode



0‡

Characteristics

Fig. 1 - DC Current Gain Characteristics

Fig. 2 - Input Voltage (on) Characteristics

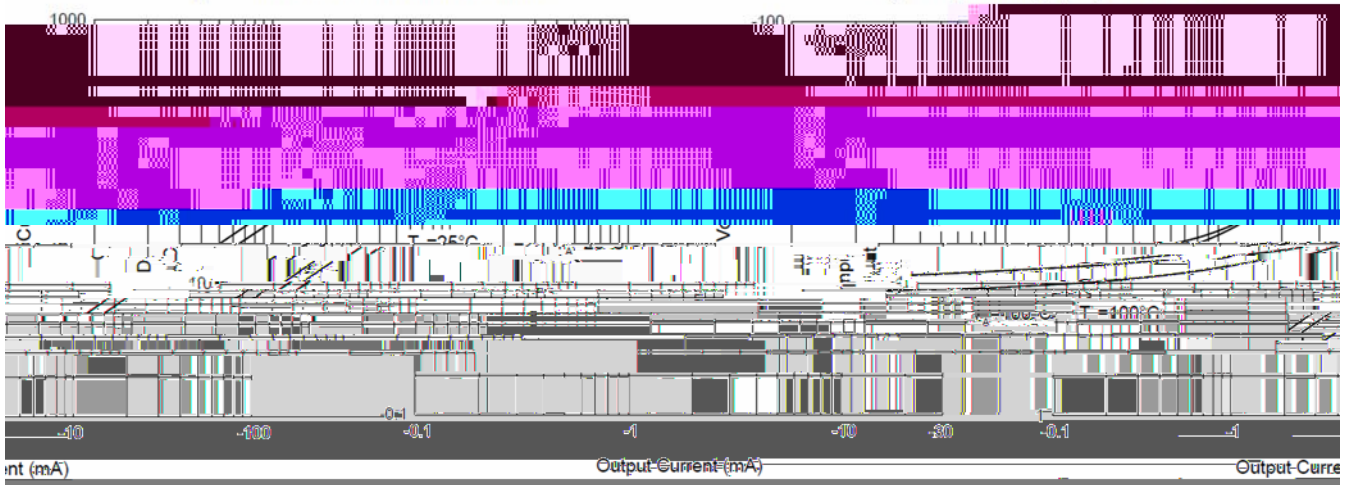


Fig. 1 - DC Current Gain Characteristics

Fig. 2 - Input Voltage (on) Characteristics

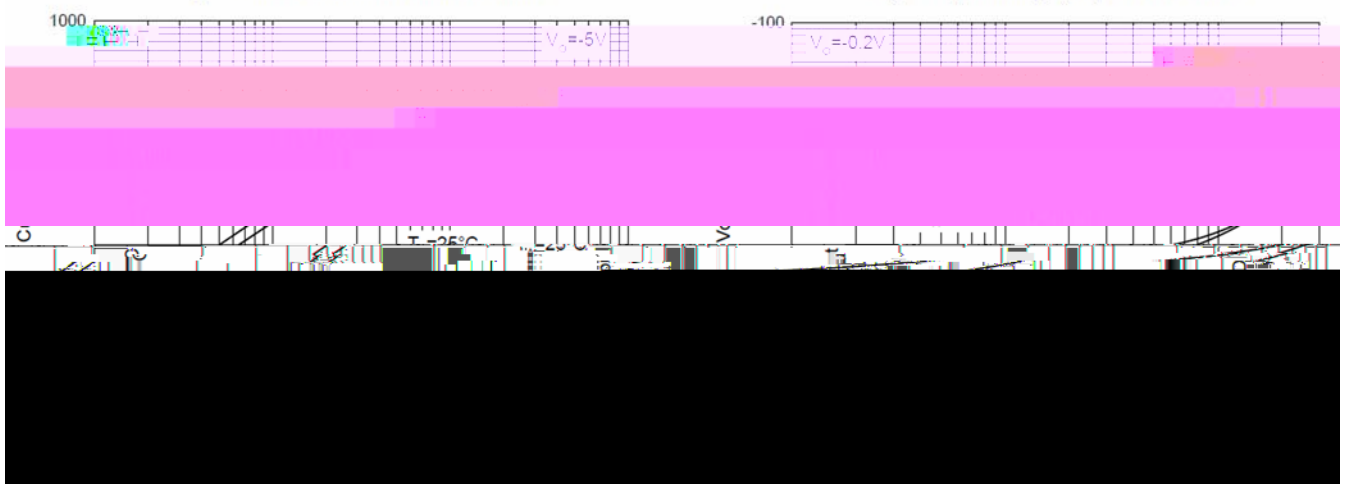
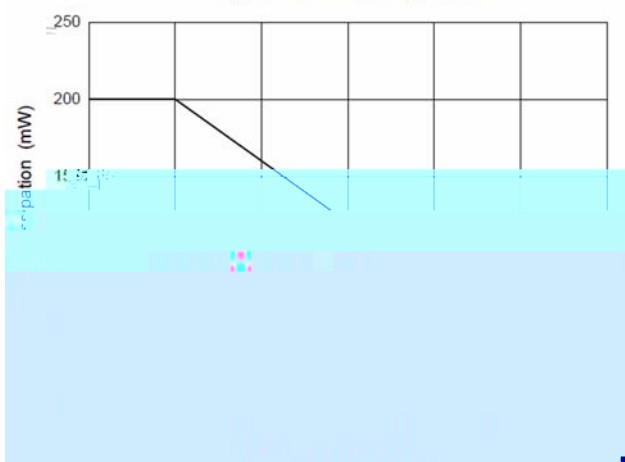


Fig. 5 - Power Derating Curve



Outline Dimensions



Note:

1. All dimensions are in millimeters (mm), unless otherwise specified.

[所有尺寸均以毫米为单位，除非另有说明]

2. General tolerances, unless otherwise specified, are as follows:

1. 通用公差为 0.15 mm，除非另有说明。

3. Dimensions and tolerances per ASME Y14.5M-2018.

[尺寸和公差遵循 ASME Y14.5M-2018 标准]

4. All dimensions shall be within 0.15 mm in maximum, unless otherwise specified.

5. All dimensions shall be within 0.15 mm in maximum, unless otherwise specified.

6. All dimensions shall be within 0.15 mm in maximum, unless otherwise specified.

7. All dimensions shall be within 0.15 mm in maximum, unless otherwise specified.

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