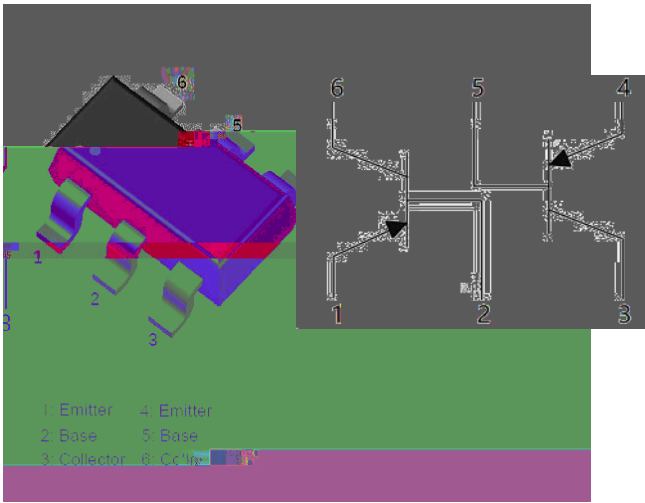




Dual PNP Small Signal Transistor



Features

- z Moisture sensitivity level 1
- z Halogen free and RoHS compliant
- z Surface mount package ideally suited for automatic Insertion

Application

- z Signal amplification
- z Switching circuit

Mechanical data

- z **Package** SOT-23-6L
- z **Terminals** Tin plated leads, solderable per J-STD-002 and JESD22-B102

Maximum Ratings (Ta=25 Unless otherwise specified)

	G ma V c`	I b] h	7 c b X] h] c b g`	J U` i Y
Device marking code				5Ft
Collector-base voltage	V _{CB0}	V	I _C = -10uA, I _E = 0	-80
Collector-emitter voltage	V _{CE0}	V	I _C = -10mA, I _B = 0	-65
Emitter-base voltage	V _{EB0}	V	I _E = -10uA, I _C = 0	-5
Collector current	I _C	mA		-100
Power dissipation	P _D	mW		200
Operation junction temperature	T _J	-		-55 to +150
Storage temperature	T _{STG}	-		-55 to +150



BC856DS

0”

Item	Symbol	Unit	Conditions	Min	Typ	Max
Collector-base breakdown voltage	$V_{(BR)CBO}$	V	$I_C = -10\mu A, I_E$			





BC856DS

Ordering Information

Prefered P/N	Packing Code	Unit weight(mg)	Minimum package(pcs)	Inner box quantity(pcs)	Outer carton quantity(pcs)	Delivery mode
BC856DS	F2	Approximate 15.6	3000	30000	120000	7" reel

Outline Dimensions



SYMBOL	INCHES		MILLimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.012	0.030	0.300	0.500
B	0.059	0.067	1.500	1.700
C	0.010	0.010	0.254	0.254
D	0.010	0.010	0.254	0.254
E	0.010	0.010	0.254	0.254
F	0.010	0.010	0.254	0.254
G	0.010	0.010	0.254	0.254
H	0.010	0.010	0.254	0.254
I	0.010	0.010	0.254	0.254
J	0.010	0.010	0.254	0.254
K	0.010	0.010	0.254	0.254
L	0.010	0.010	0.254	0.254
M	0.010	0.010	0.254	0.254

Suggested Pad Layout

