

YJQ50N03A

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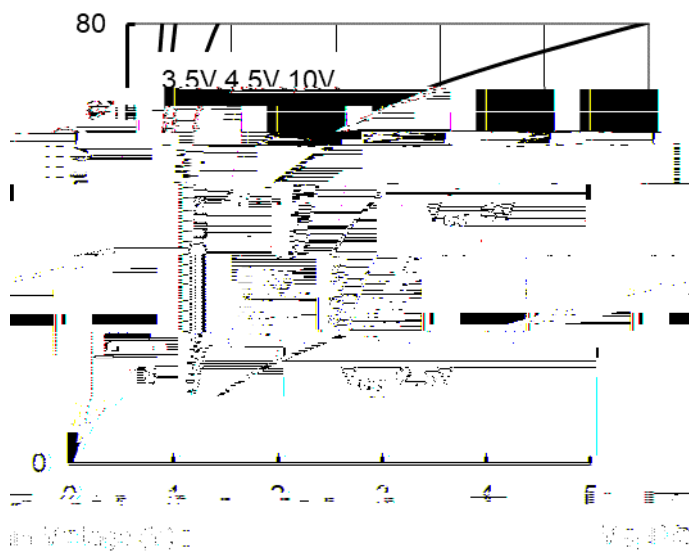
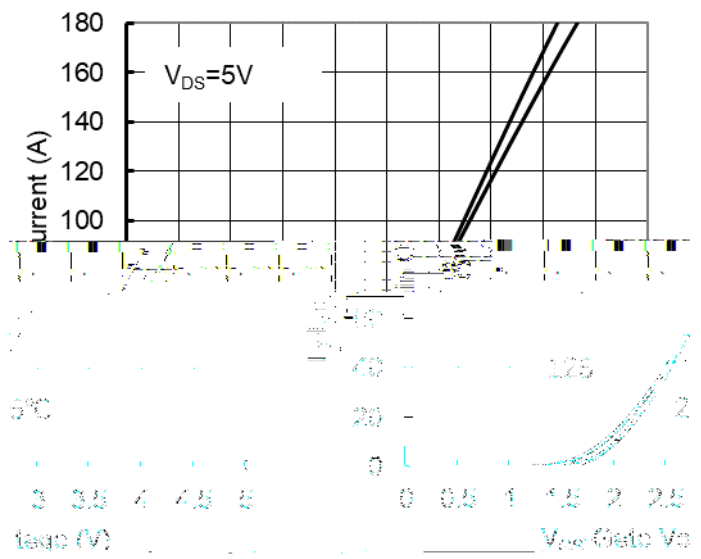
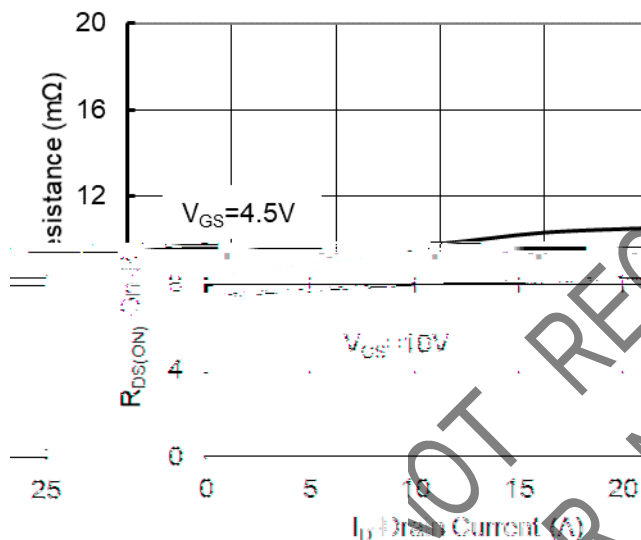
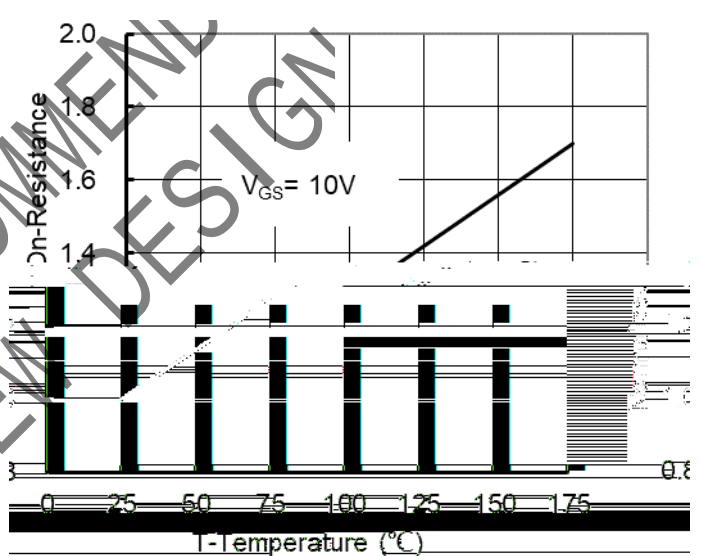
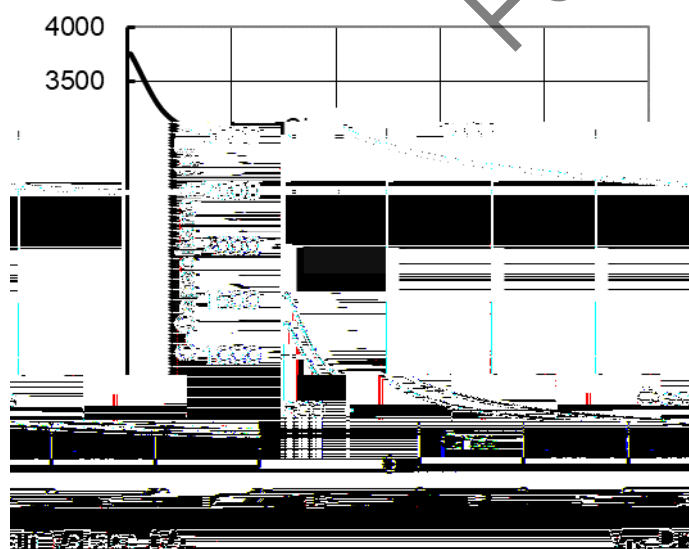
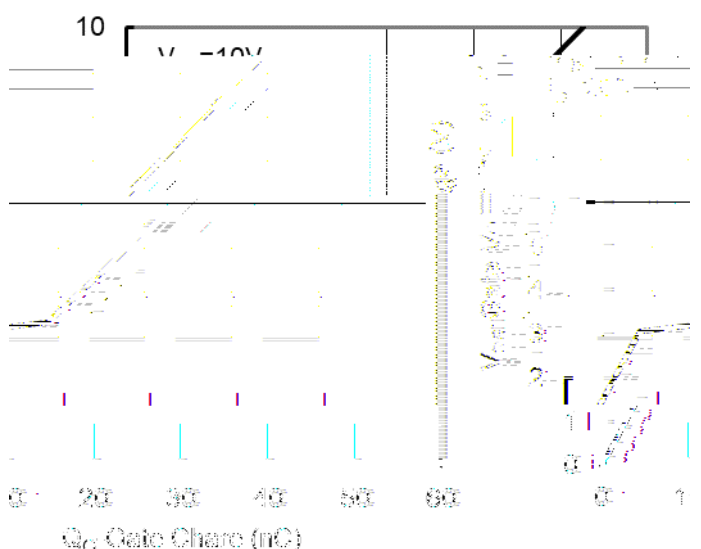
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Electrical Characteristics (T_j=25 unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			1	
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ± 20V, V _{DS} =0V			± 100	nA

Gate Threshold Voltage V

**Typical Performance Characteristics****Figure1. Output Characteristics****Figure2. Transfer Characteristics****Figure 3: On-Resistance vs. Drain Current and Gate Voltage****Figure 4: On-Resistance vs. Junction Temperature****Figure5. Capacitance Characteristics****Figure6. Gate Charge**

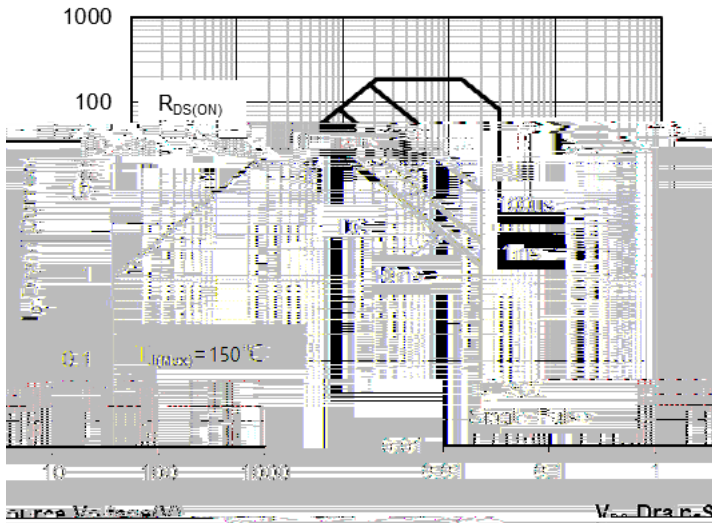


Figure7. Safe Operation Area

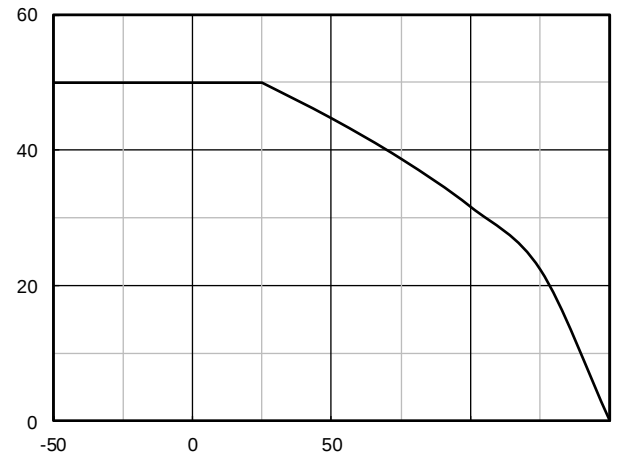


Figure8. Maximum Continuous Drain Current
vs Case Temperature

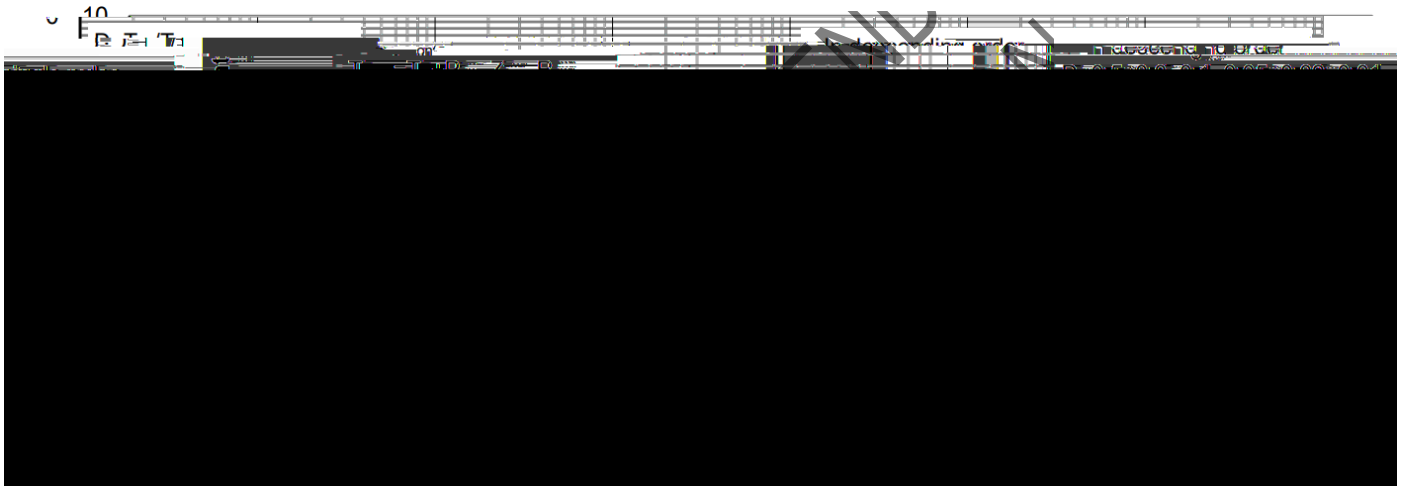
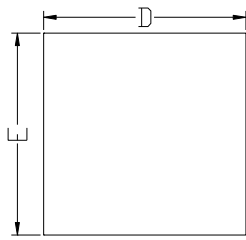


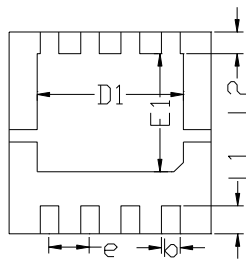
Figure9. Normalized Maximum Transient Thermal Impedance



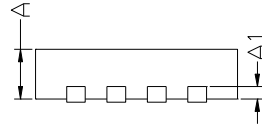
DFN3333-8L Package information



Top View

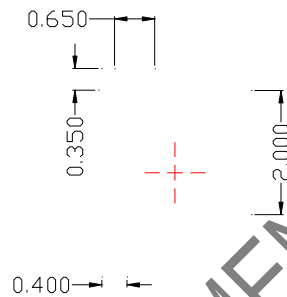


Bottom View



Side View

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
D	3.15	3.25	3.35
E	3.15	3.25	3.35
A	0.70	0.80	0.90
A1		0.20 BSC	
A2			0.10
D1	2.20	2.35	2.50
E1	1.80	1.90	2.00
L1	0.35	0.45	0.55
L2		0.35 BSC	
b	0.20	0.30	0.40
e		0.65 BSC	



Suggested Solder Pad Layout
Top View

Note:

- 1.Controlling dimension:in millimeters.
- 2.General tolerance: +/-0.10mm.
- 3.The pad layout is for reference purposes only.



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