



N-Channel and P-Channel Complementary Power MOSFET



Product Summary

NMOS(Die1)

V_{DS} \dot{A} \dot{A} yNO S(= M

$R_{DS(ON)}$ (at $V_{GS}=4.5V$) 55 mohm

$R_{DS(ON)}$ (at $V_{GS}=2.5V$) 75 mohm

PMOS(Die2)

V_{DS} -20V

I_D -1.5A

$R_{DS(ON)}$ (at $V_{GS}=-4.5V$) 120 mohm

$R_{DS(ON)}$ (at $V_{GS}=-2.5V$) 170 mohm

General Description

- Trench Power LV MOSFET technology
- High Density Cell Design for Low $R_{DS(ON)}$
- High Speed switching
- Part no. with suffix "Q" means AEC-Q101 qualified

Applications

- Interfacing, Logic switch
- Load switch
- Power management

Absolute Maximum Ratings ($T_A=25$

			1 & KDQC	3 & KDQC	8QLW
Drain-source Voltage		V_{DS}	20	-20	V
Gate-source Voltage		V_{GS}	± 10	± 10	V
Drain Current	$T_A=25$	I_D	2.0	-1.5	A
	$T_A=100$		1.3	-1.0 =25	

		P_D	595	568	mW
	$T_A=100$		238	227	mW
Thermal Resistance Junction-to-Case @ Steady State ^C		R_{JA}	210	220	/ W
Junction and Storage T_e			-55~+150	-55~+150	

Ordering Information (Example)

PREFERED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJJ2429AQ	F2	2429	3000	30000	120000	7" reel



YJJ2429AQ

N-MOS Electrical Characteristics ($T_J=25$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	
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N-MOS Typical Performance Characteristics

Figure1. Output Characteristics

Figure2. Transfer Characteristics

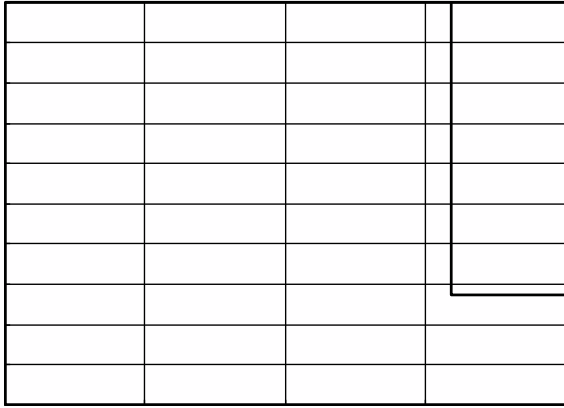


Figure 7. $R_{DS(on)}$ VS Drain Current

Figure 8. Forward characteristics of reverse diode

Figure 9. Normalized breakdown voltage

Figure 10. Normalized Threshold voltage

Figure 11. Maximum Transient Thermal Impedance

Figure 12. Safe Operation Area

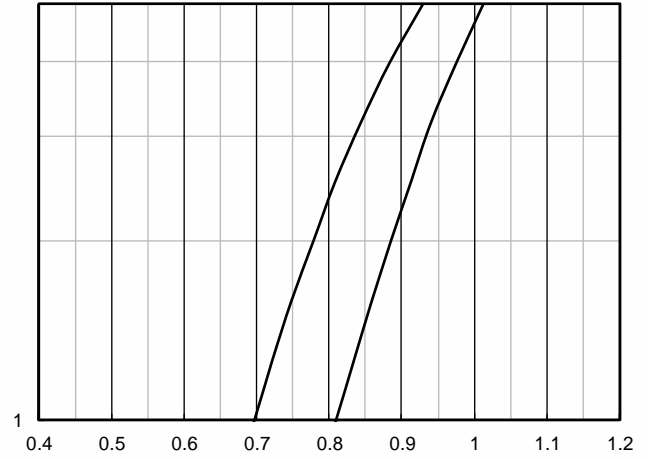


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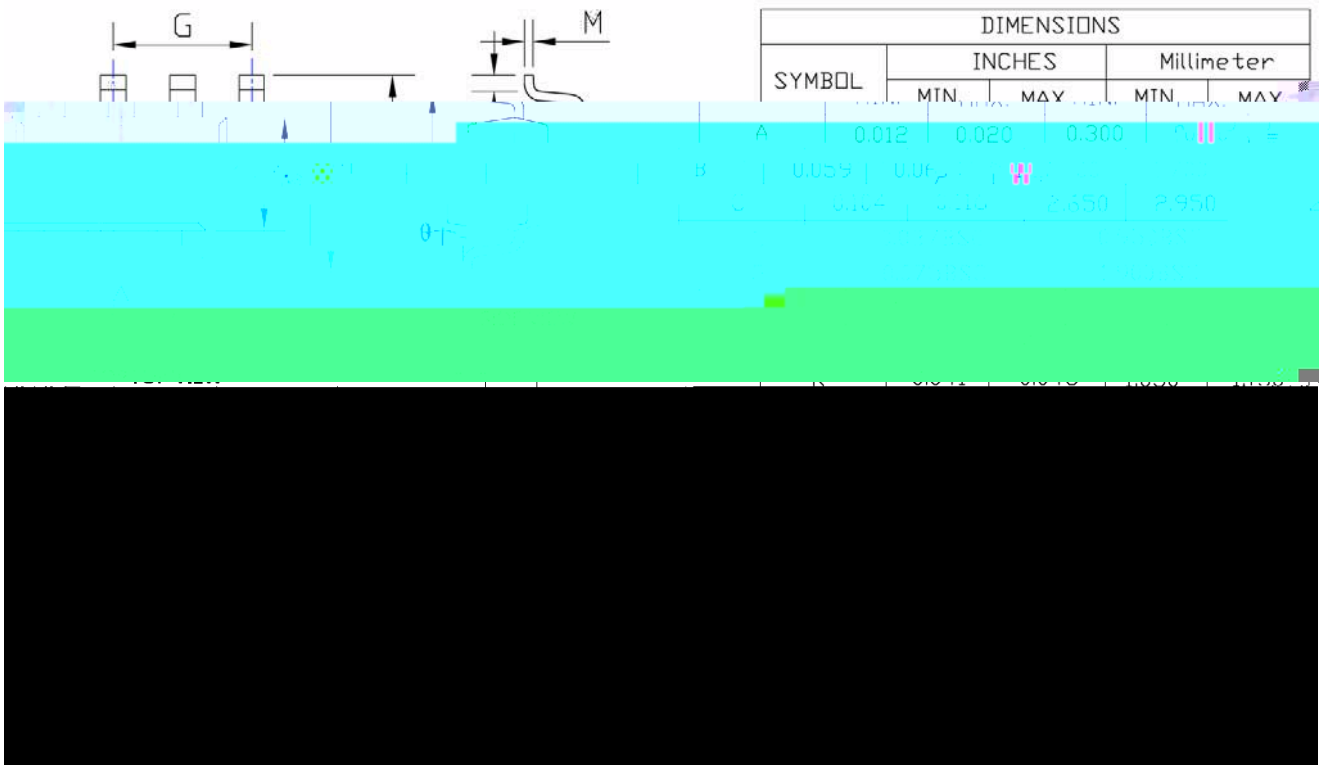
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Figure 12. Safe Operation Area



YJJ2429AQ

SOT-23-6L Package Information



YJJ2429AQ

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